**Face-To-Face Student/Supervisor Meeting Record**

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| **Project Title:** | Design and Implementation of a Multi-core Processor using FPGA | **Photo:** |  |
| **Student Name:** | Matteo Bovino | **Student ID:** | 8671055 |
| **Supervisor:** | Server Kasap | **Student UID:** |  |
| **Supervisor UID:** |  | **Department:** | AAEEE |
| **Course Code:** | Electrical and Electronic Engineering | **Module Code:** | 305AAE / 306AAE |
| **Date Today:** | 23/10/2020 | **Time:** | 09:30 AM |

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| **Current Progress and Issues:** | |
| *In this meeting we discussed about the literature review and the materials to study. With regards to the literature review, I was shown the main sources which I should use to find relative researches and reports. These sources include the IEEE Explore portal, books and reference manuals. Currently I am studying multiple aspects of my project in parallel, however I was advised to prioritize certain fields in order to have a better progression. One of the agreed points of the meeting was to focus on the workshops and HDL in order to start experimenting with the Vivado software. This will be crucial, as most of the developing will take place in this simulation environment.* | |
| **Agreed Key Action Points:** | |
| *The main upcoming deadline is the one for the literature review (16/11/2020). Although there is still plenty of time, this task will require a lot of researching and editing thus we decided to start early on. As agreed during the meeting, it will be written using multiple sources and citing detailed research papers. Meanwhile, the other material will be covered in the specified manner in order to remain on track on every front.* | |
| **Date and Time of next meeting:** | 09:30 AM 06/11/2020 |

*Signatures of those present:*

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| **Supervisor:** |  |
| **Student: Matteo Bovino** |  |